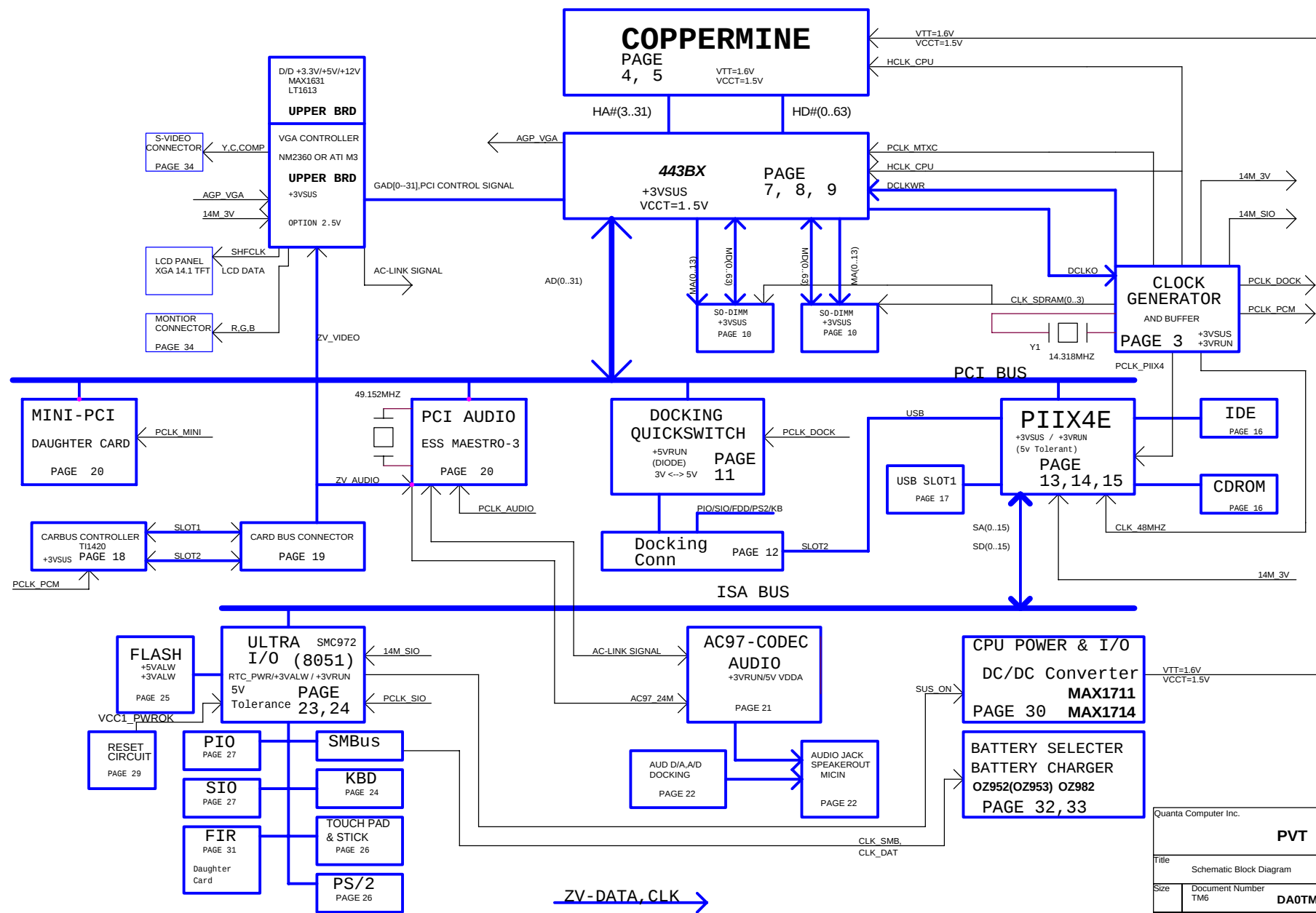


TM6



INDEX

Pg#	Description	DNI LIST
1	Schematic Block Diagram	
2	Index,DNI,power & grounds	
3	Clock Generator	
4-5	Pentium II Processor	
6	PII Bypass & GTL Pullup	
7-9	443BX	
10	System DRAM Expansion (144P-SODIMM X 2)	
11	Docking PCI Buffers	
12	Docking Station Conn.	
13-15	Mobile Triton II - MPIIX4E(PIIX4M	
16	CDROM & IDE Conn. and PIIX4E pullups	
17	USB , MISC AND IDE BUS DAMPING RESISTOR	
18	Cardbus Controller PCI 1225(PCI 1420	
19	Cardbus Connector	
20	Mini-PCI & MAESTRO 3I	
21	AC97 Codec	
22	Audio & COM1 Connector	
23-24	Ultra I/O Controller FDC37N972(TIKI)	
25	Flash & EEPROM	
26	Stick & Touch Pad & PS2	
27	Serial & Parallel	
28	Power Switch	
29	Reset Circuit & LED Drive	
30	CPU Power	
31	RTC Battery,DC-IN FIR	
32	Smart Battery Selector	
33	Smart Charger	
34	S-Video & CRT Connector	
35	SCREW,PAD	
36	VGA & POWER,LED Upboard Connector	

Power & Grounds

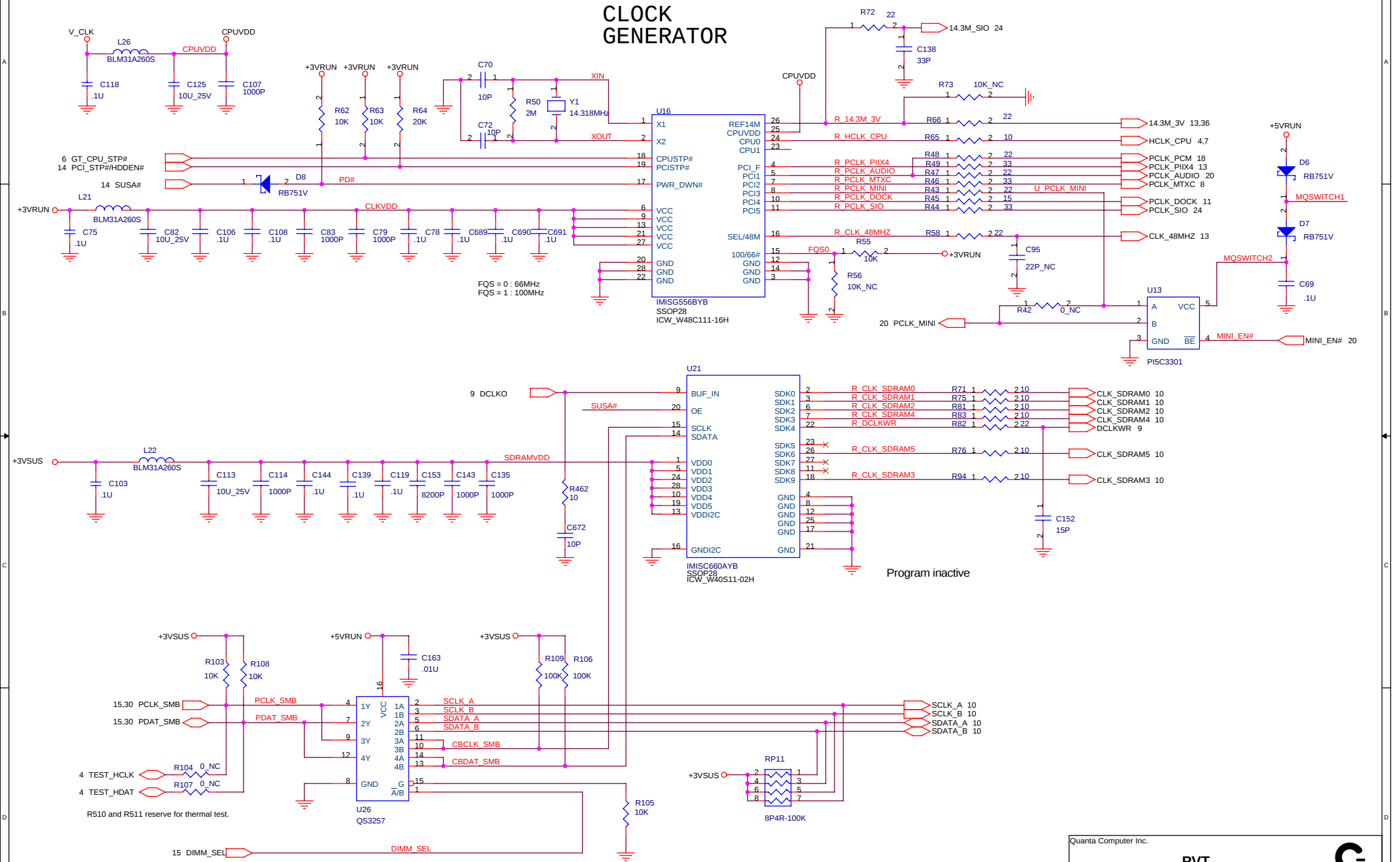
Label	Pg#	Description	Control Signal
DC_IN+	12, 35, 36, 37	AC ADAPTER (20V)	
BATT+	35, 36, 37	BATTERY CHARGE POWER	
ABATT+		MAIN BATTERY POWER	
BBATT+		SECOND BATTERY POWER	
PWR_SRC	12,30,31,32,36	MAIN POWER (10~20V)	
RTC_PWR	23, 32, 36	RTC & PCL POWER (3V)	
+12V	12,19,28,31,34,36	+12V	DRUNPWROK
VTT	5,6,30	1.6V	RUNPWROK
VCCT	4,5,6,7,30	1.5V	RUNPWROK
V_CLK	3,30	HOST I/O PLANE (2.5V)	RUNPWROK
+3VRUN	3,4,5,8,11,13,14,15,16,17,18,20,21,22,23,28,29,30,31,36	SUSB# CONTROLLED POWER	RUN_ON
+3VSUS	3,6,7,9,10,11,13,14,15,18,19,20,24,28,29,36	SUSC# CONTROLLED POWER	SUS_ON
+3VALW	7,16,20,23,24,25,31,36	8051 POWER (3V)	
+5VALW	12,25,29,30,32,33,36	SMBUS & OZ953 POWER	
+5V_BS	31,32	Battery selector Power	PWR_SRC
+5VRUN	3,4,11,15,17,19,20,26,27,28,29,31,34,36	SUSB# CONTROLLED POWER	RUN_ON
+5VSUS	7,12,19,21,27,28,29,30,34	SUSC# CONTROLLED POWER	SUS_ON
+5VHDD	16,28	HDD POWER (5V)	HDDC_EN#
+5VMOD	16,18	MODULE POWER (5V)	MODC_EN#
STRB#/5V	12,27,28	EXTERNAL FDD POWER (5V)	FDD/LPT#
+5VFAN1,+5VFAN2	28	FAN POWER (5V)	FAN1_OFF/ON# FAN2_OFF/ON#
VDDA	21,22	AUDIO ANALOG POWER (5V)	RUN_ON
 GND	ALL PAGES	DIGITAL GROUND	
 AGND	21,22,35	AUDIO GND	
 GNDP	30	CPU POWER GND	
 CGNDP	33	CHARGER GND	
 TGND	24,26	STICK GND	

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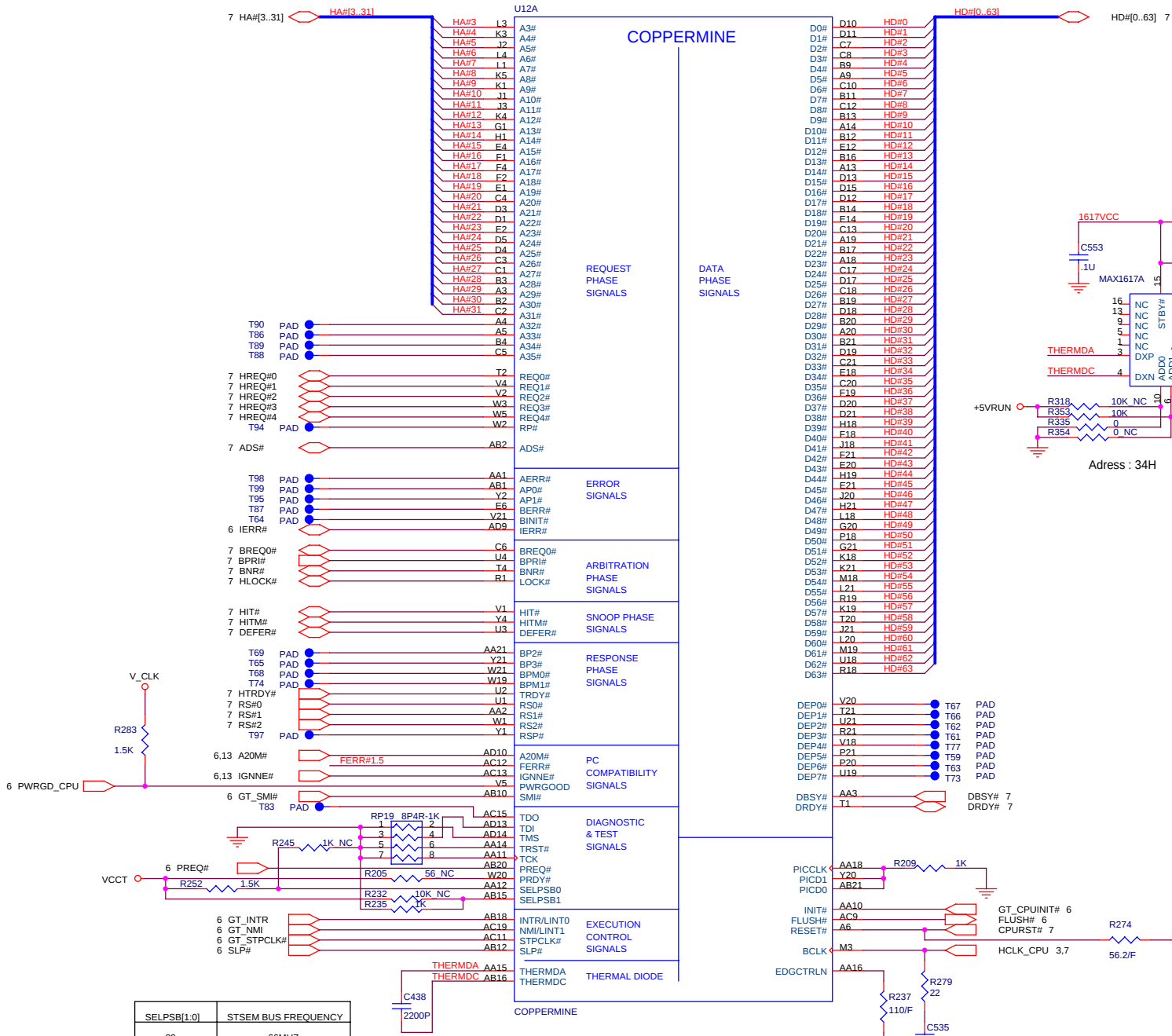
PVT

Title
Index,DNI,power & groundsSize
Document Number
TM6
DA0TM6MBAH6 Rev H
Rev
2GDate: Saturday, November 25, 2000
Sheet 2 of 36

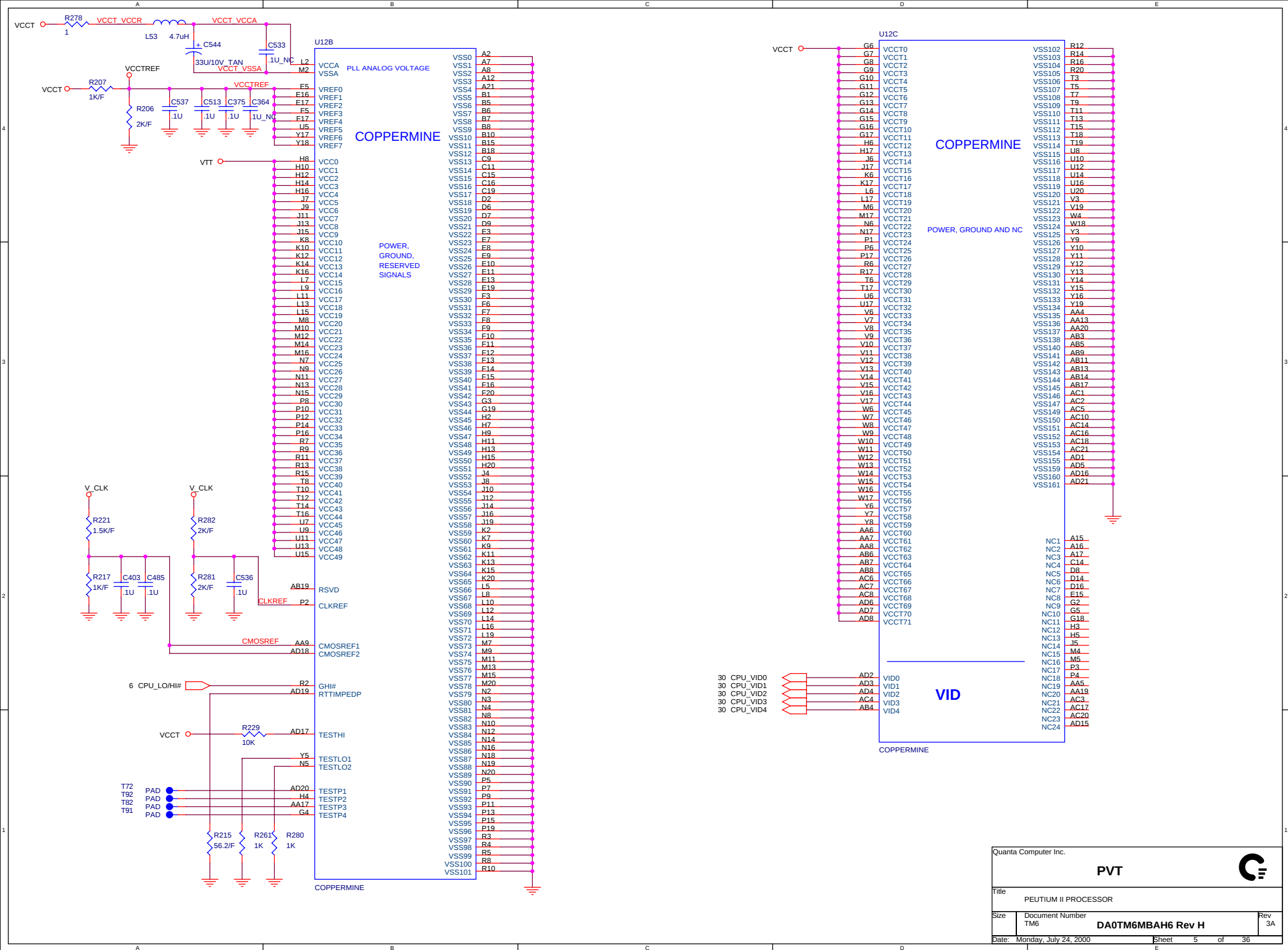
CLOCK GENERATOR

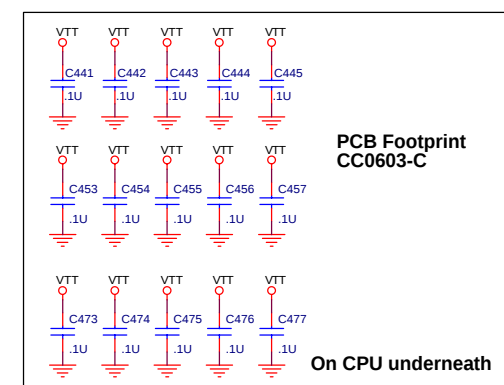
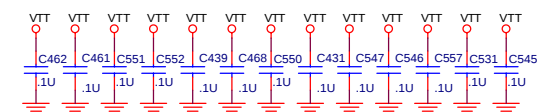
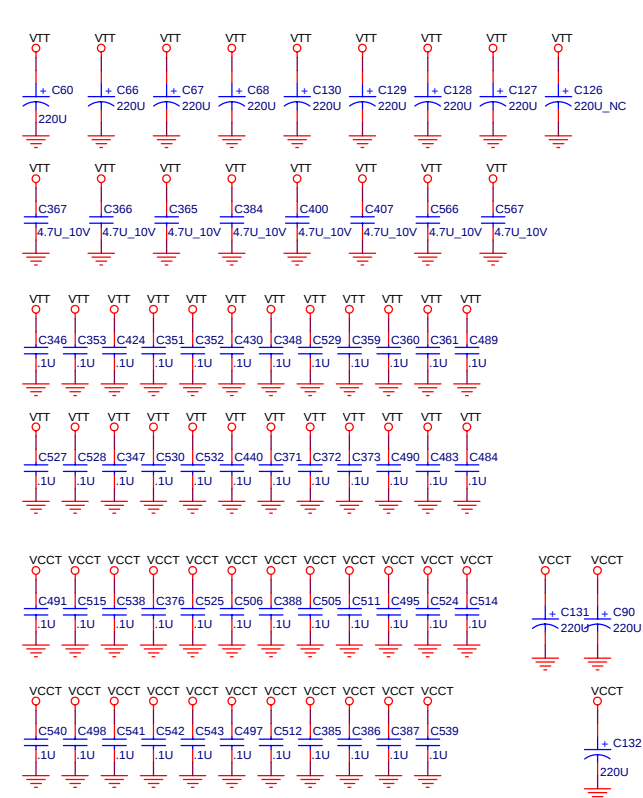


DIMM_SEL
0 <-> CKBUF-M (or CKGen) , DRAM0
1 <-> CKBUF-M (or CKGen) , DRAM1
STR <-> Isolated

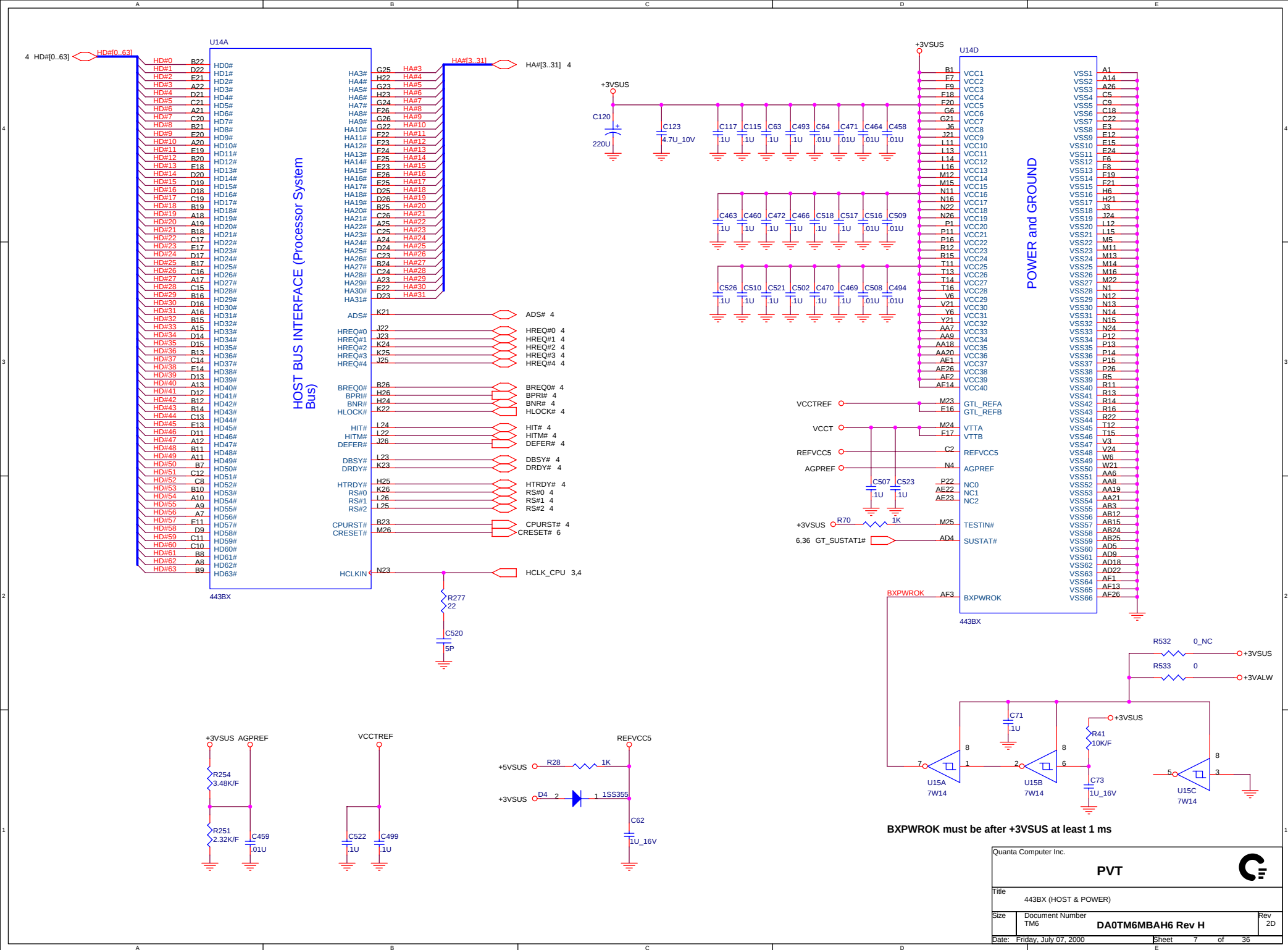


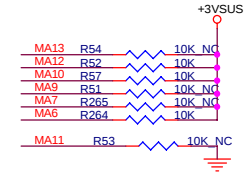
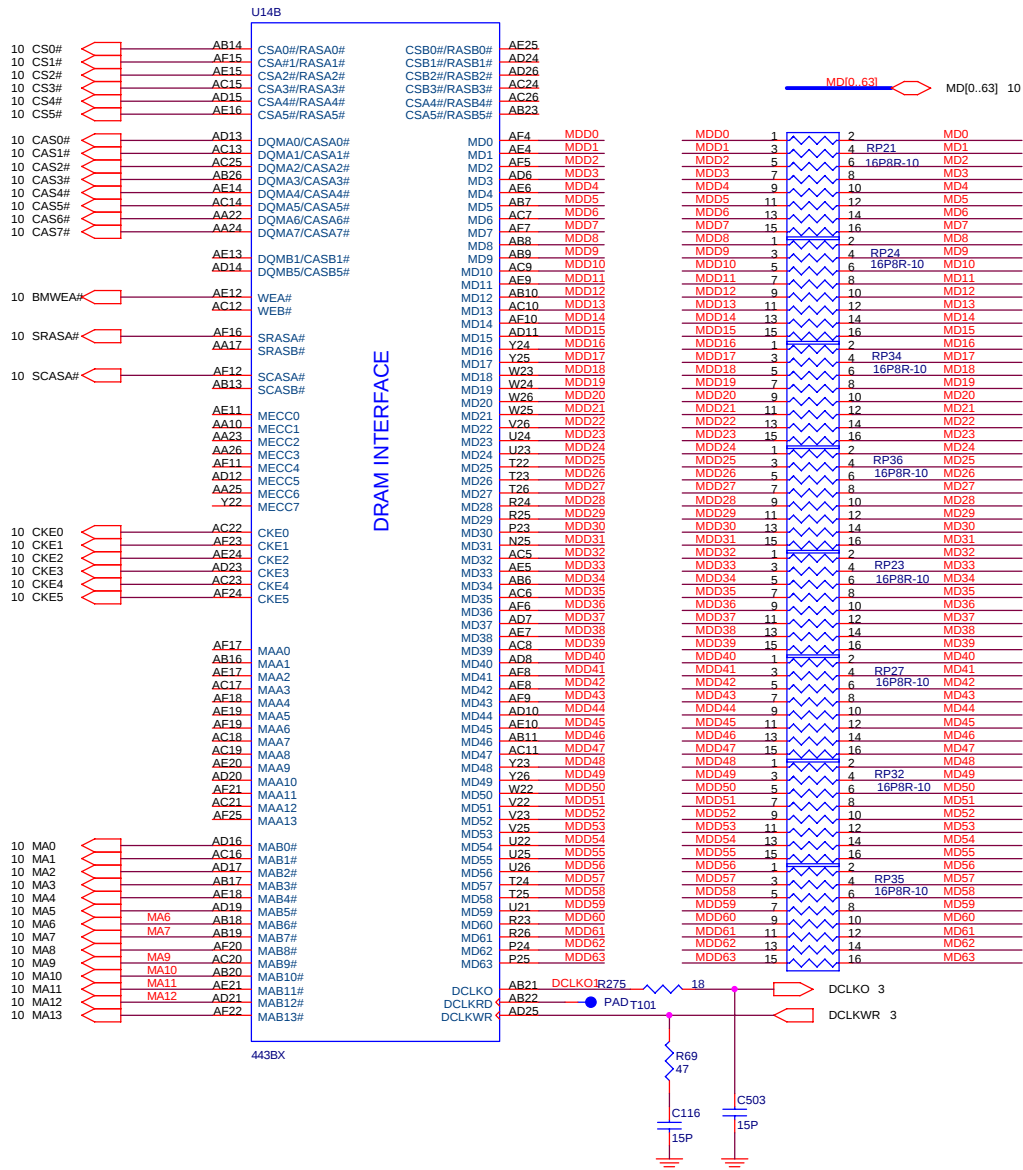
SELPSB[1:0]	STSEM BUS FREQUENCY
00	66MHZ
01	100MHZ
10	RESERVED
11	133MHZ



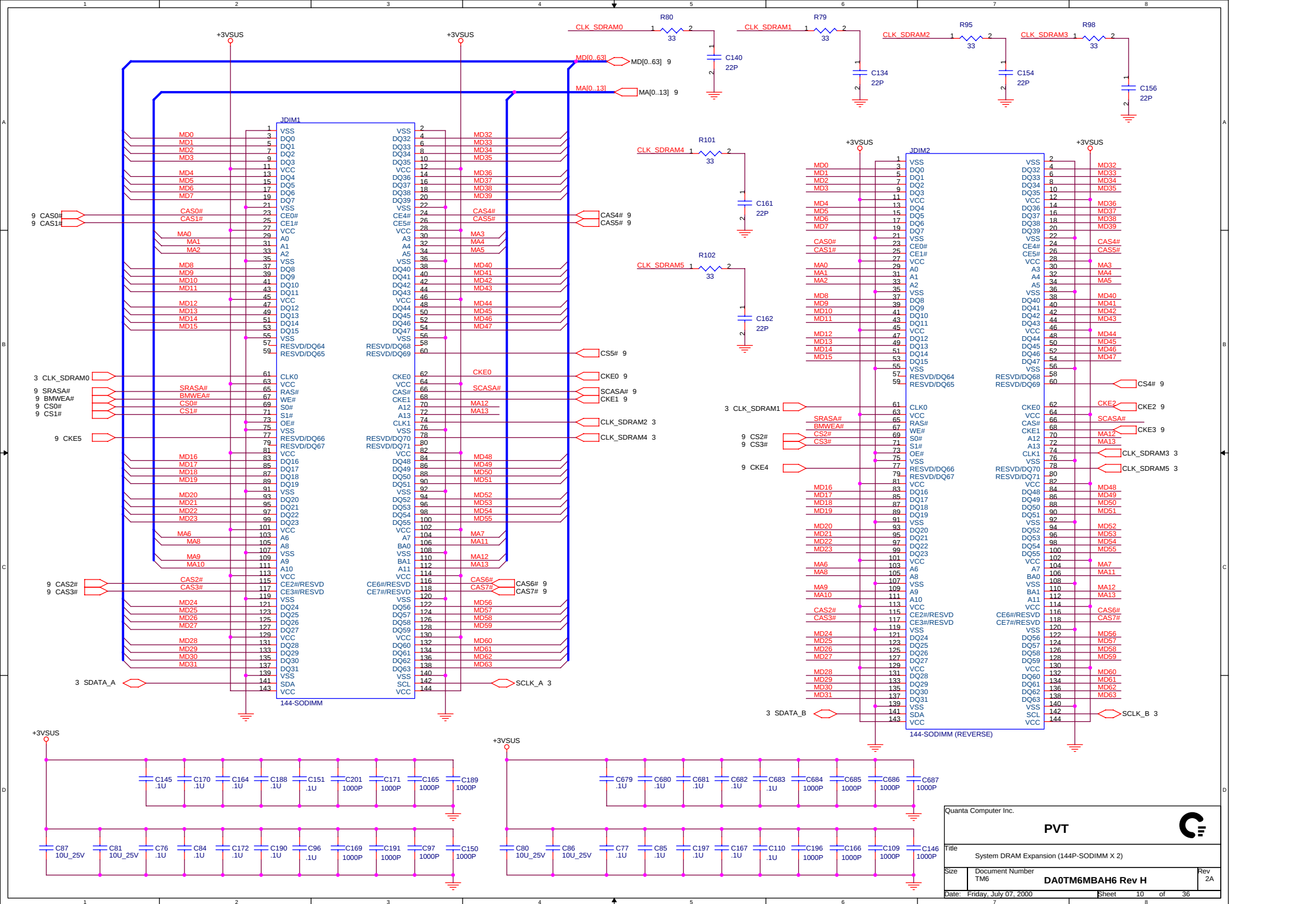


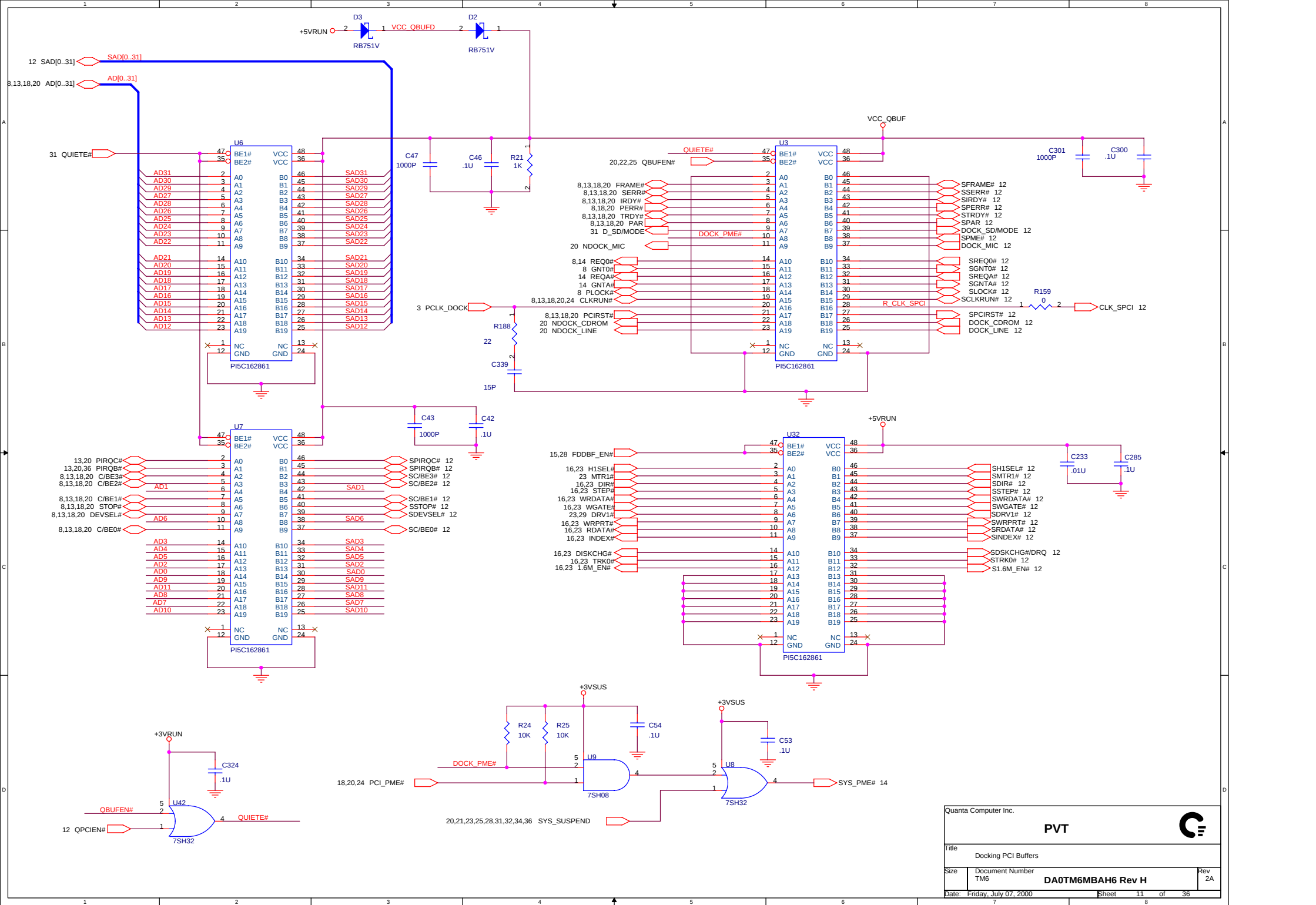
Quanta Computer Inc.		PVT		C=	
Title					
ASIC & BYPASS					
Size	Document Number	DA0TM6MBAH6 Rev H			Rev
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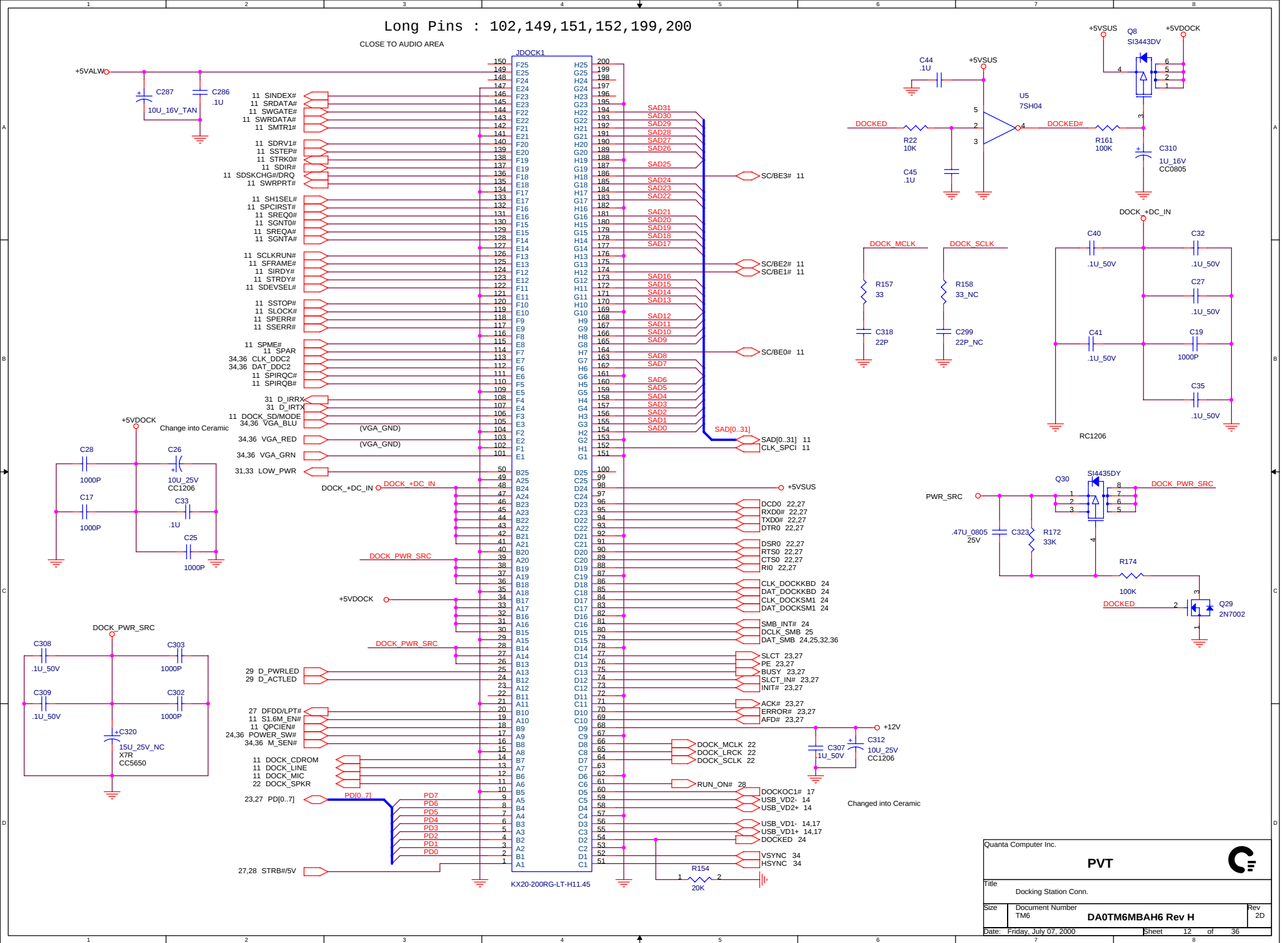
Pin Name	Function	Low	High	Internal Resistor	Status Register
MAB12#	Host Frequency Select	66MHz	100MHz	Pull-down	NBXCFC[13]
MAB11#	In-Order Queue Depth Enable	1 (no pipelining)	4 (max)	Pull-up	NBXCFC[2]
MAB10#	Quick Start Select	Stop Clock Mode	Quick Start Mode	Pull-down	PMCR[3]
MAB9#	AGP Disable	AGP Enabled	AGP Disabled	Pull-down	PMCR[1]
MAB7#	MM Configuration	Normal operation	Tri-states certain memory signals	Pull-down	DRAMC[5]
MAB6#	Host Bus Buffer Mode Select	Desktop GTL+	Mobile Low Power GTL+	Pull-down	none

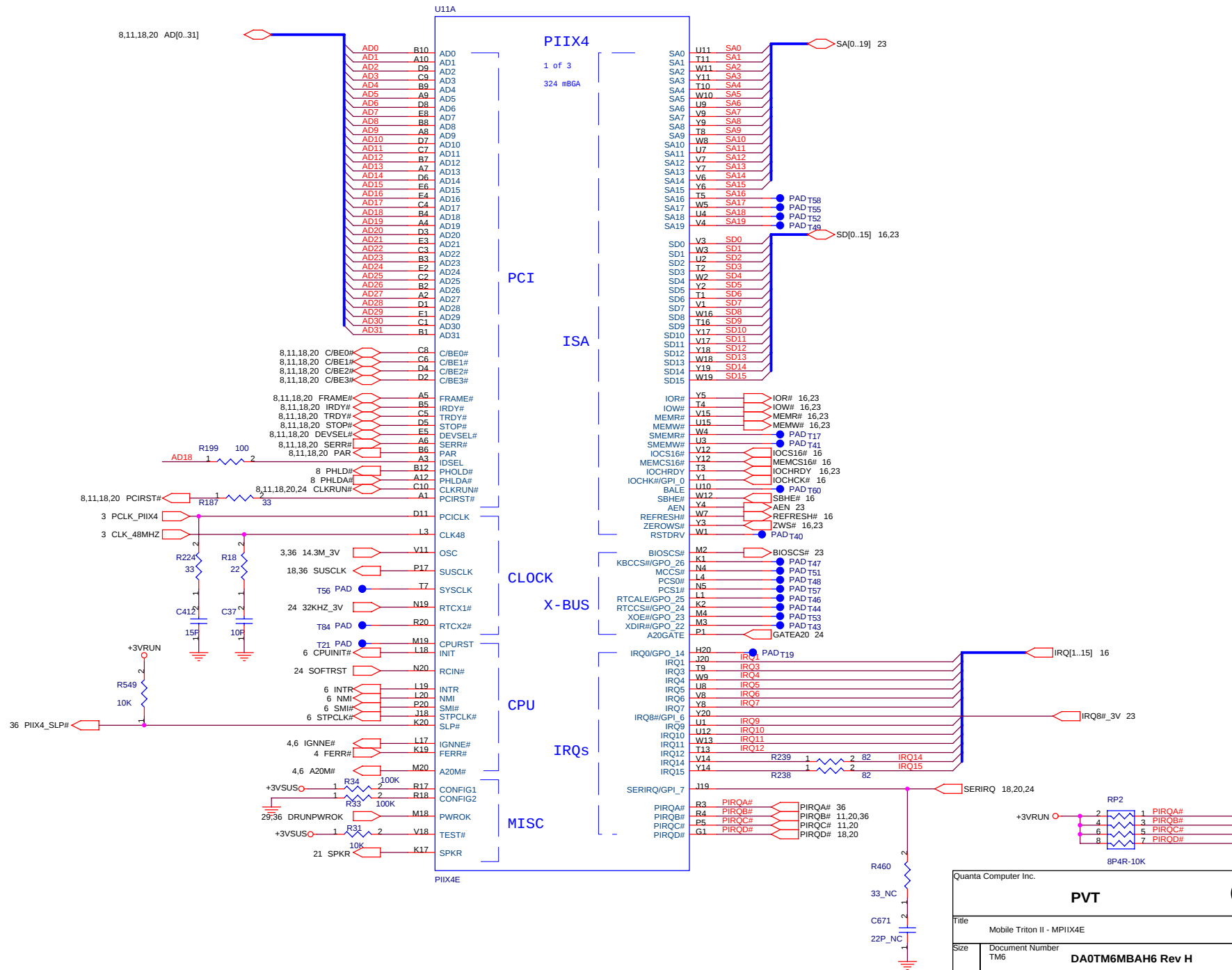


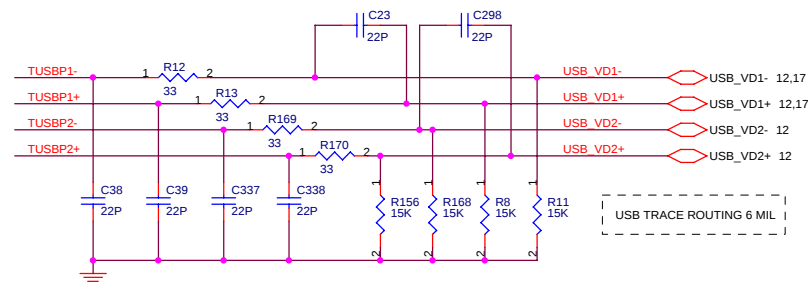


Long Pins : 102,149,151,152,199,200

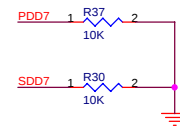
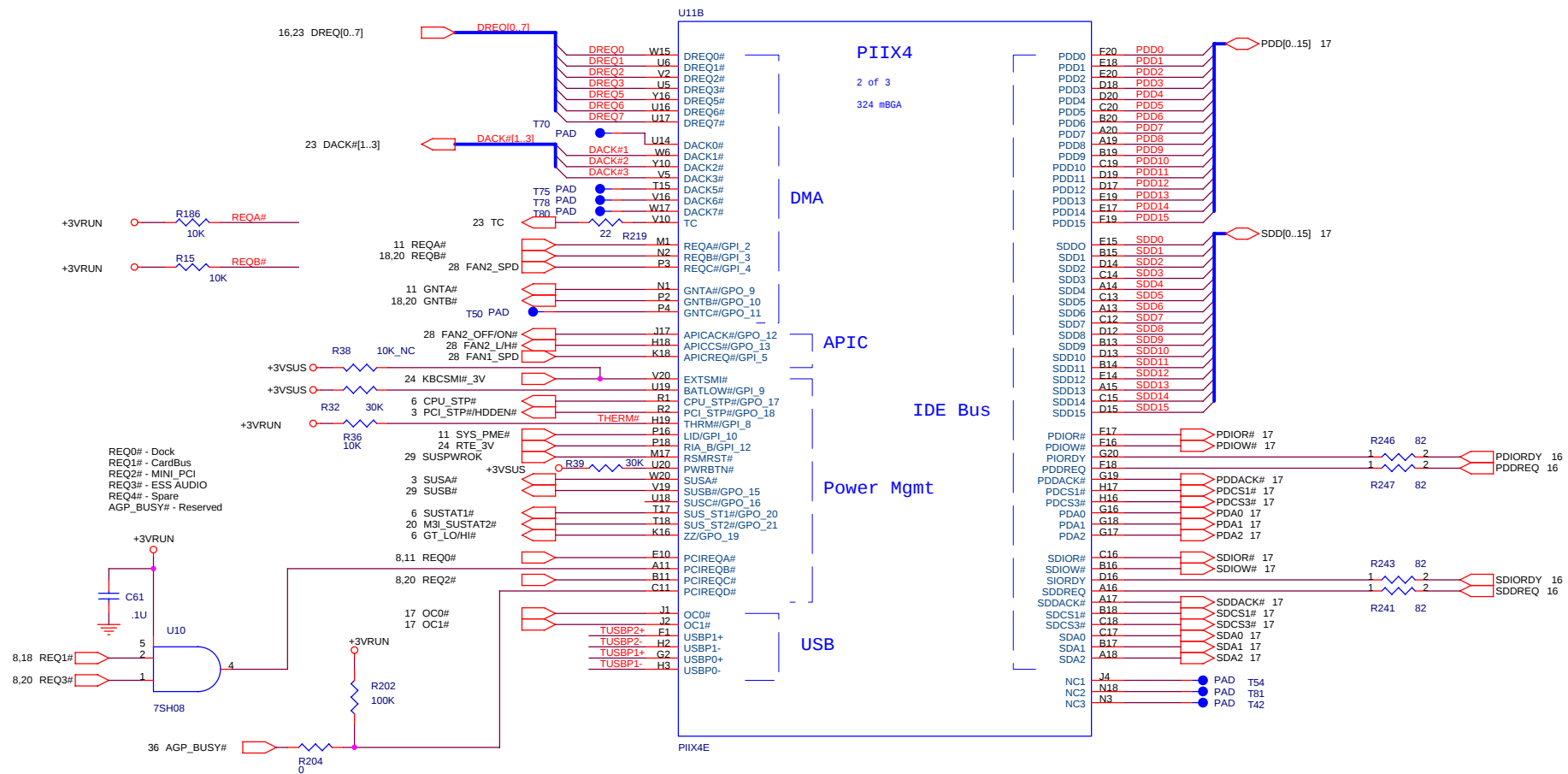
CLOSE TO AUDIO AREA

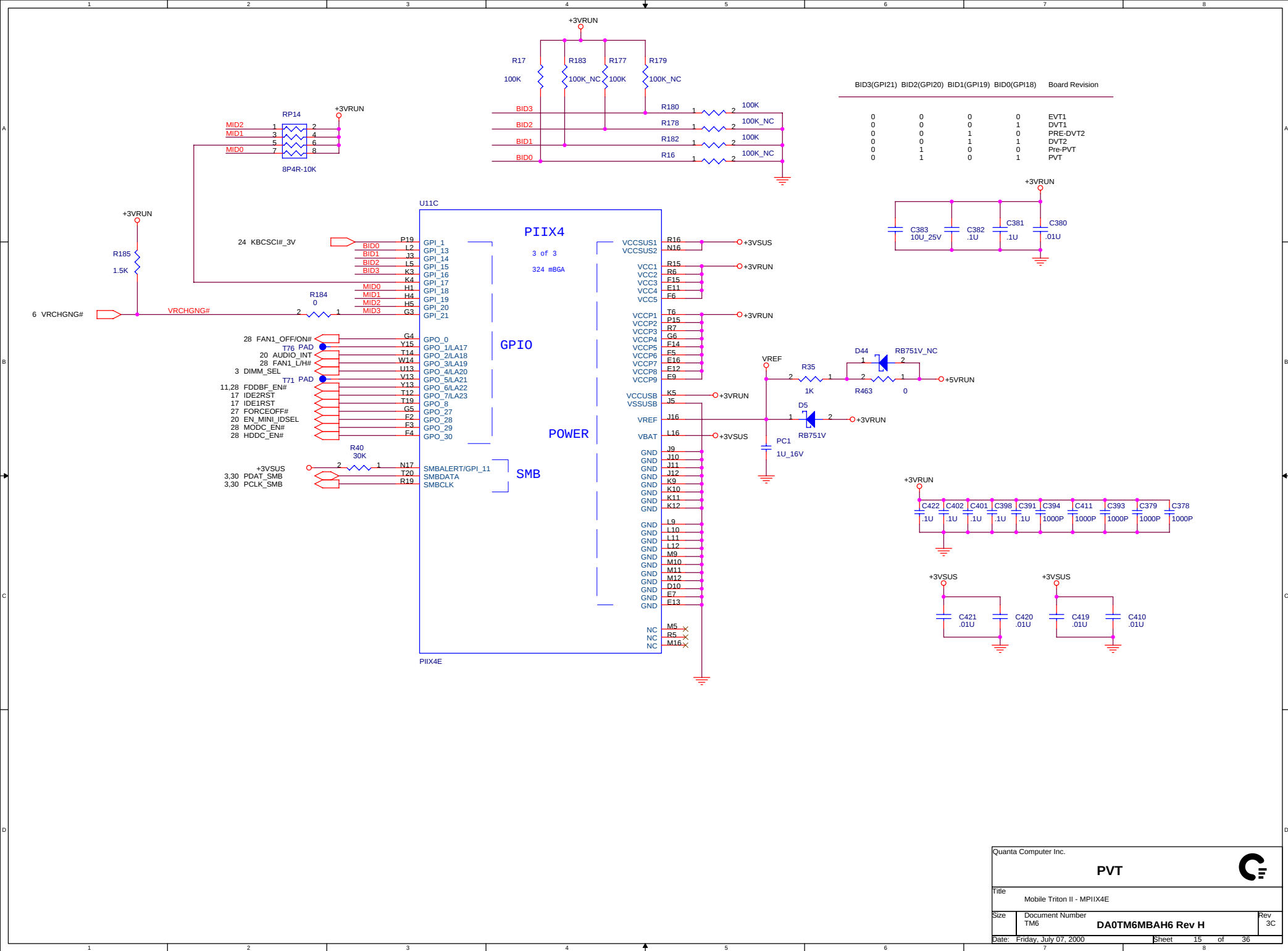


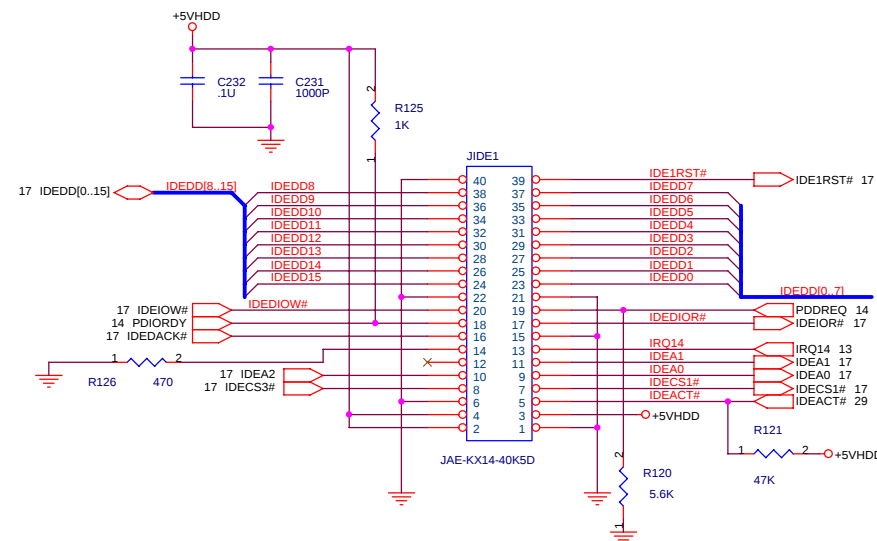
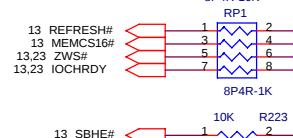
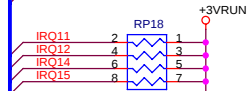
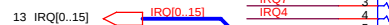
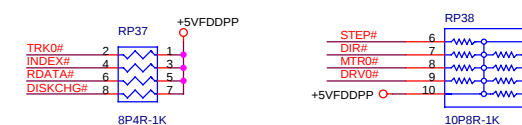


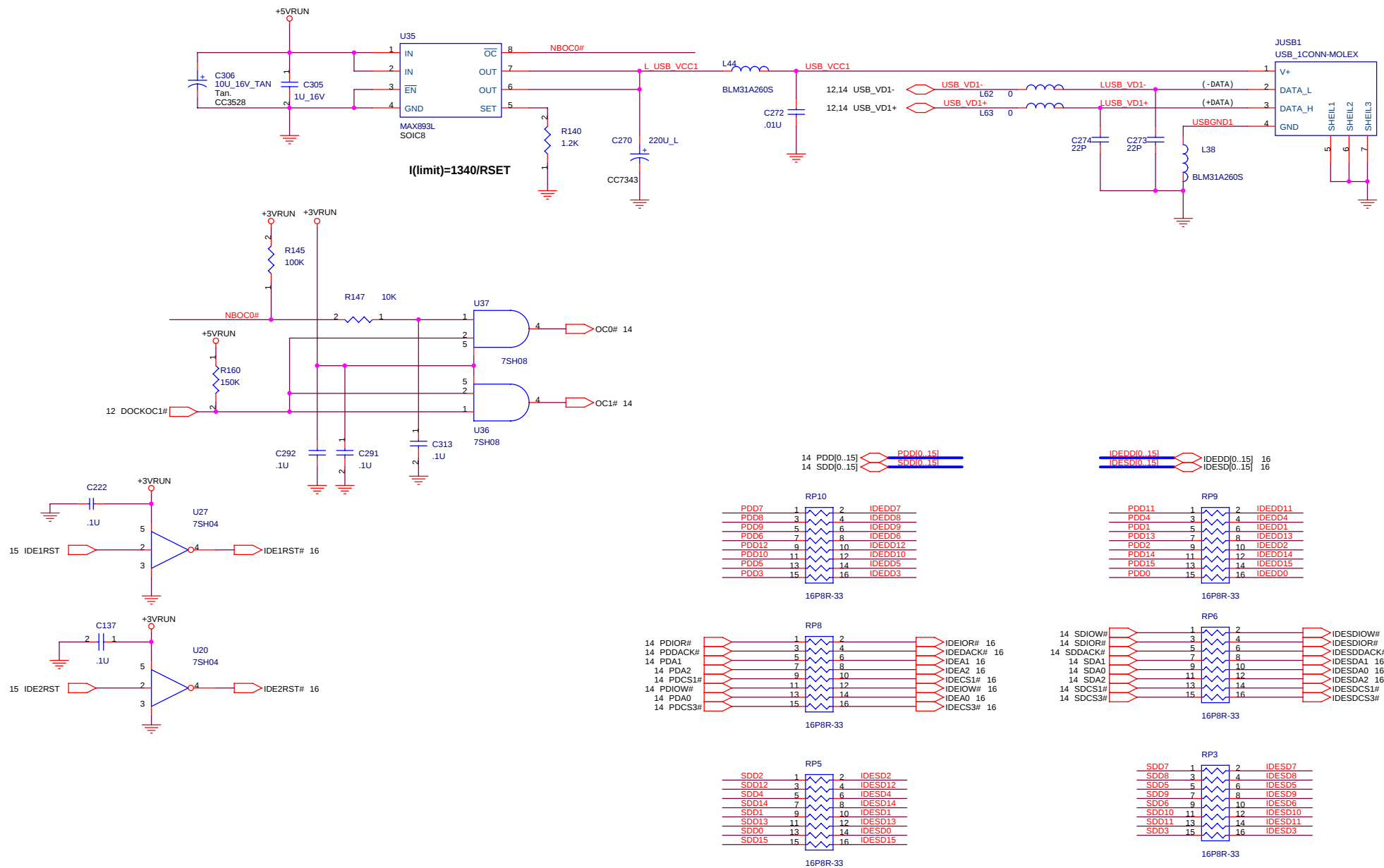


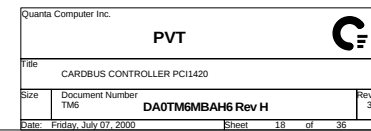
USB Host Termination should be close to PIIX4

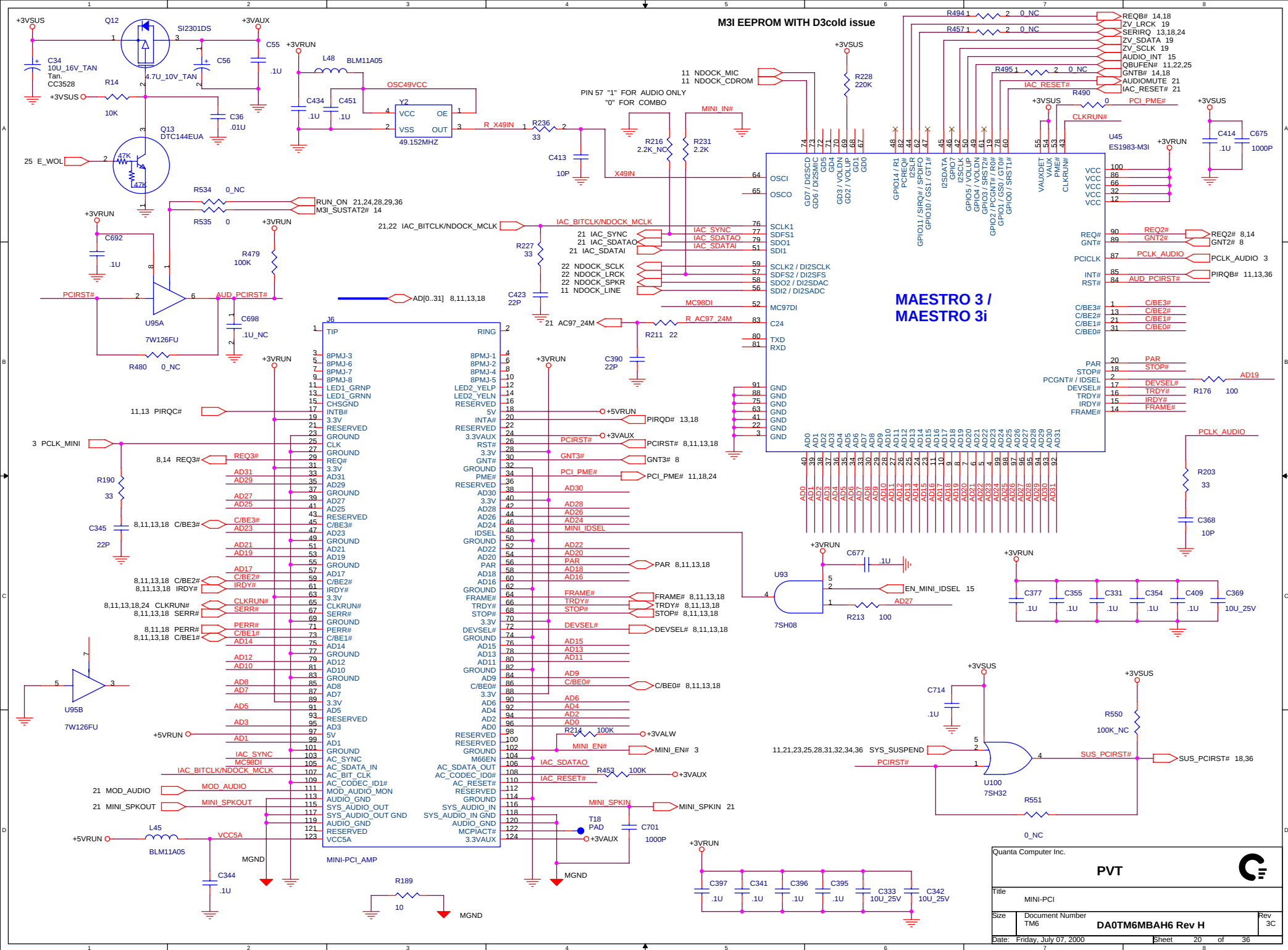


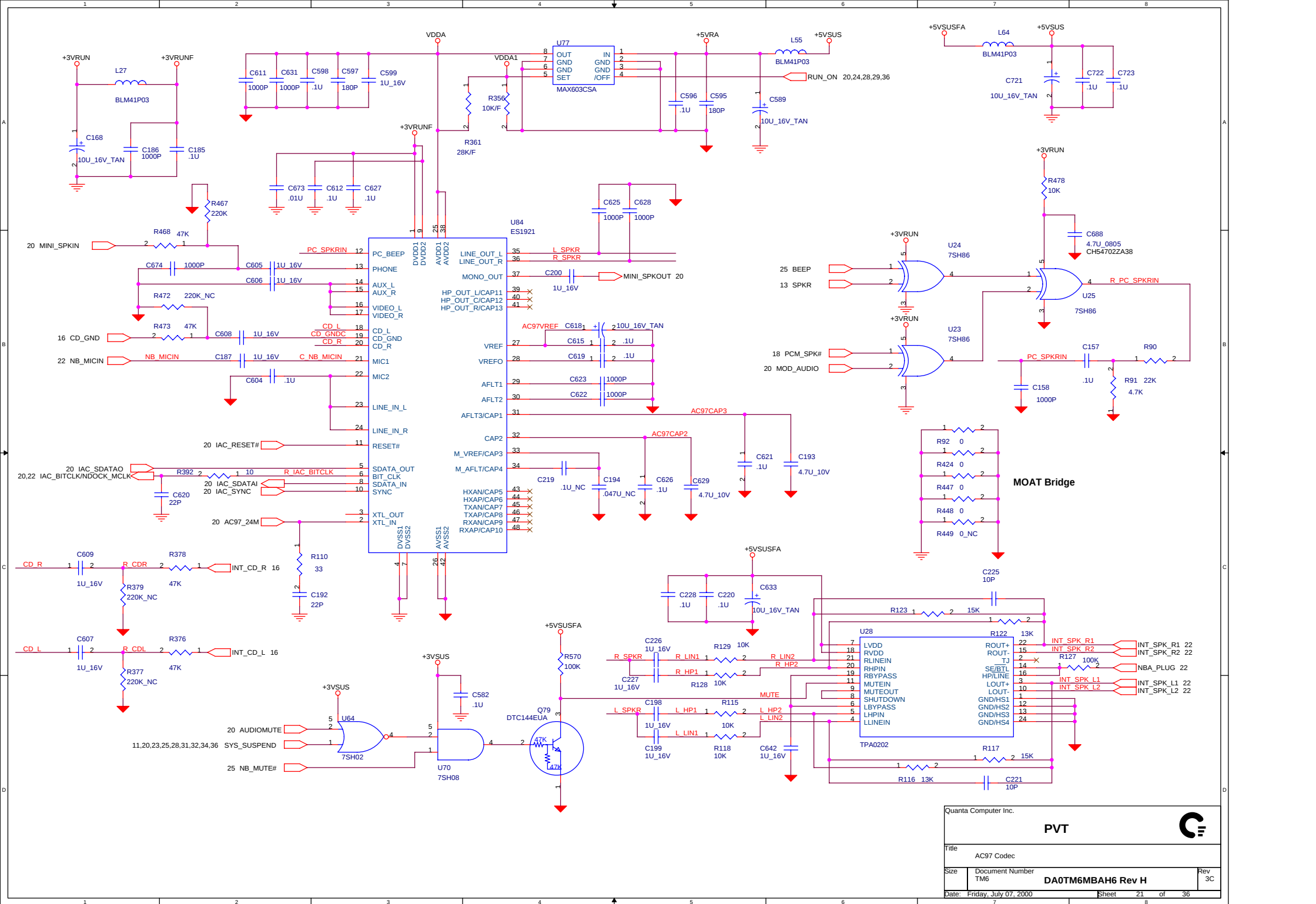


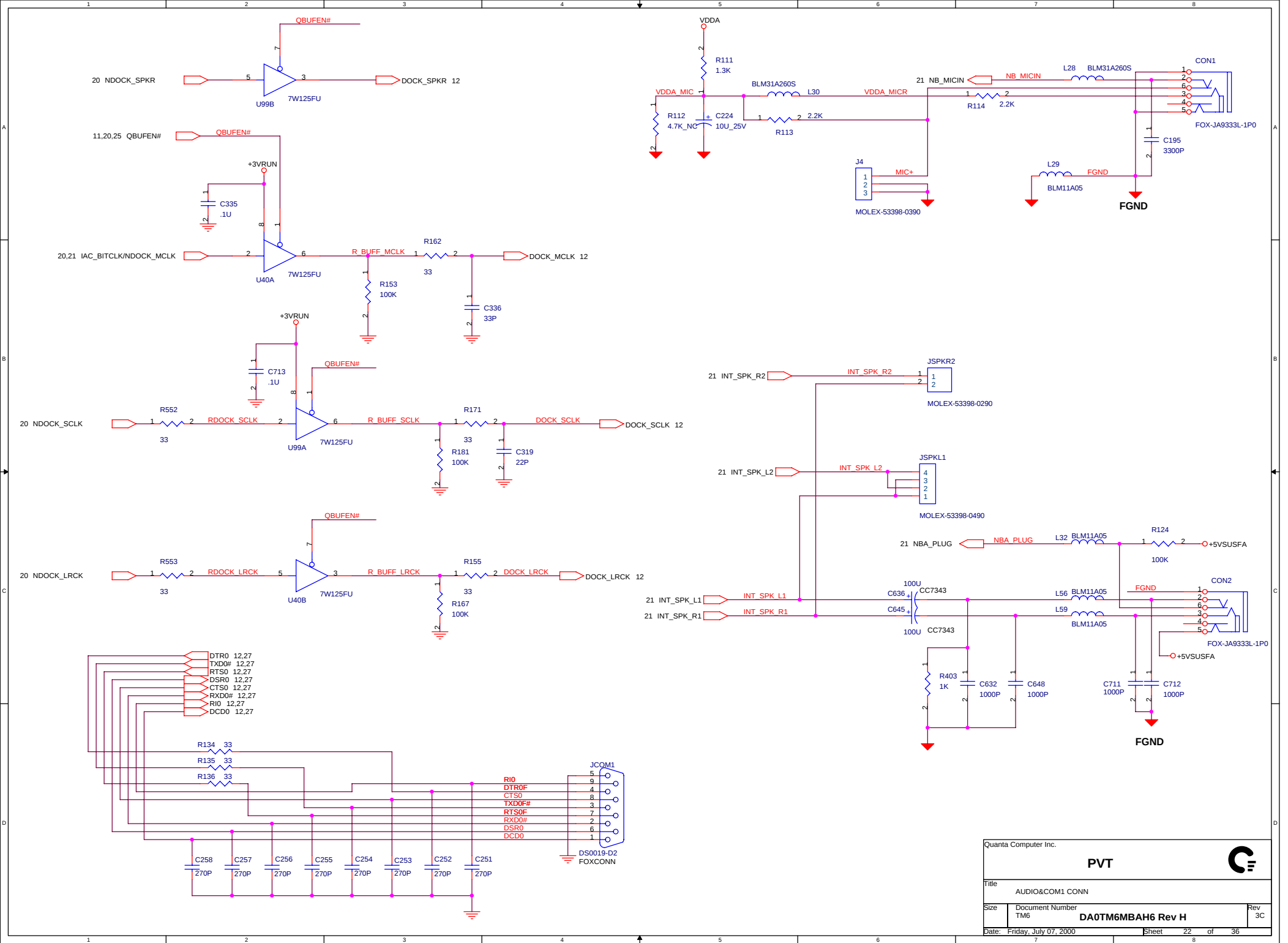


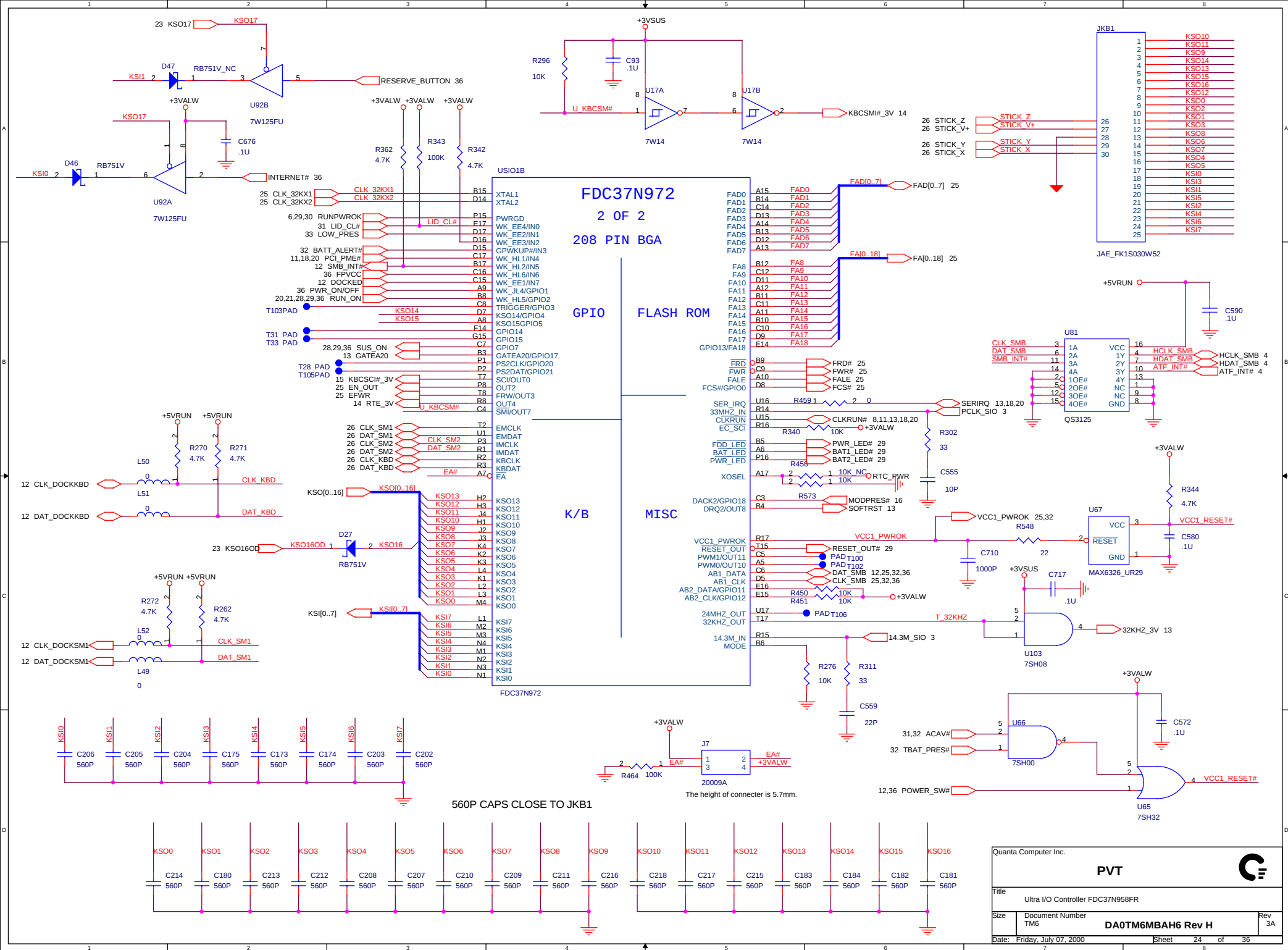


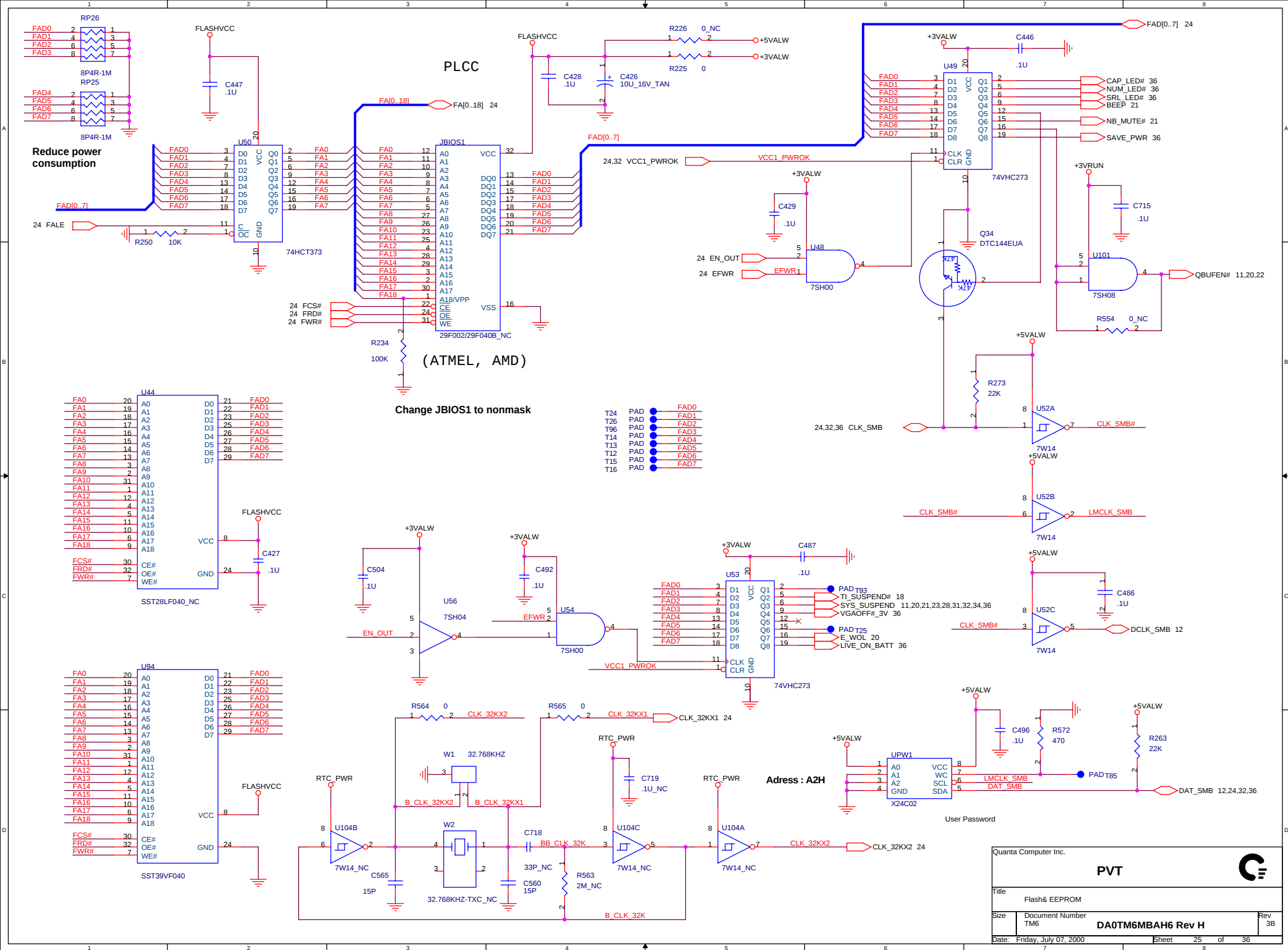




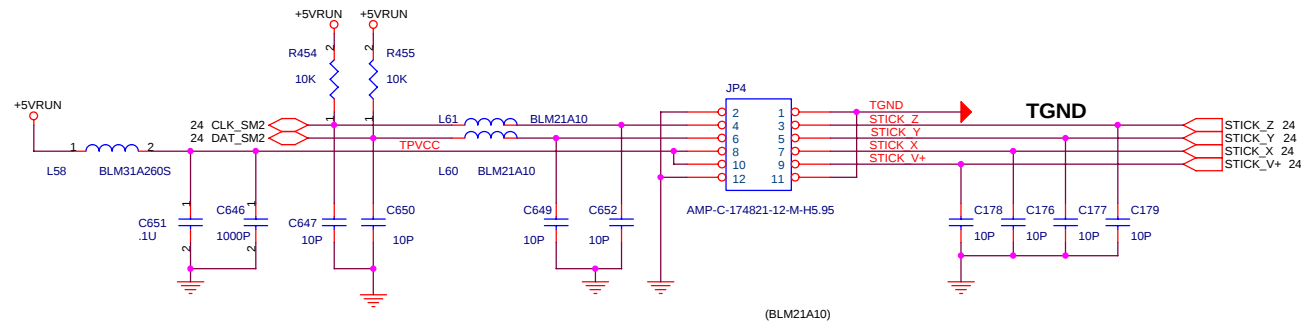




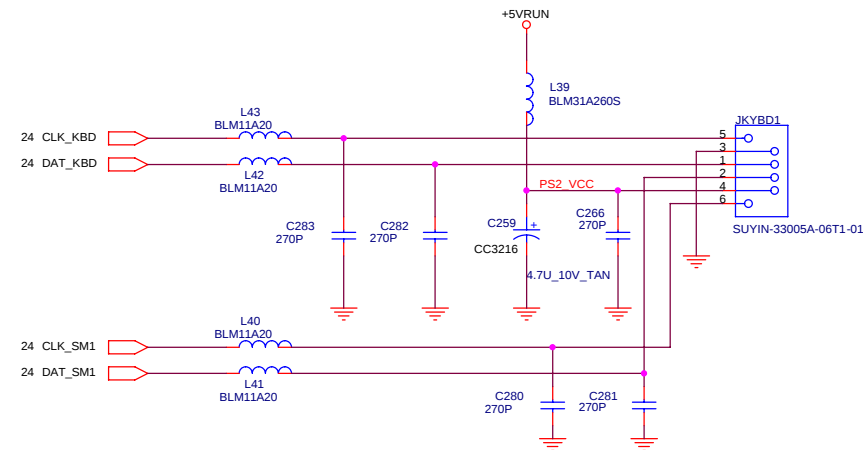




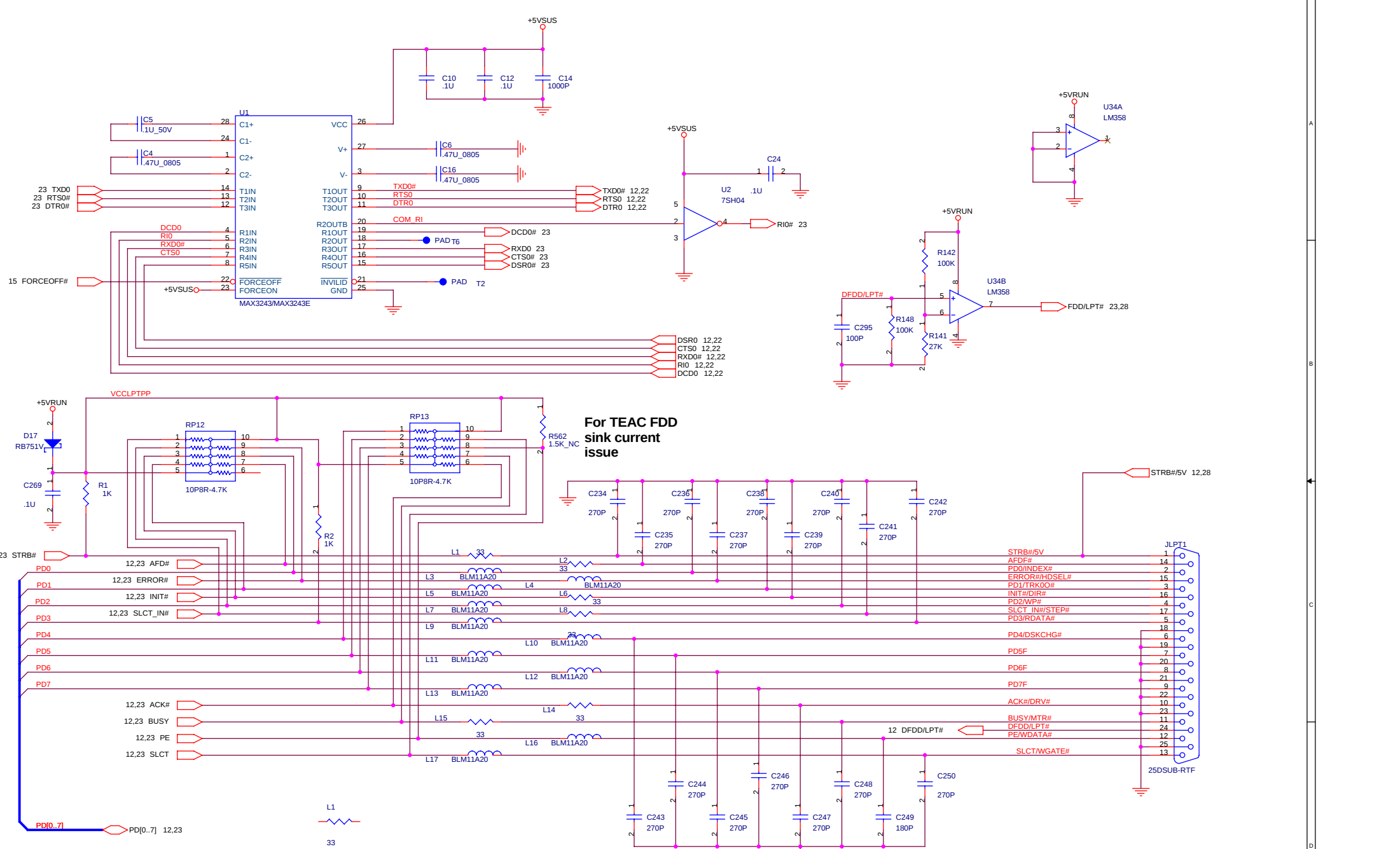
Touch Pad



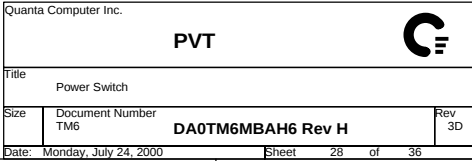
TIE GND TO H20

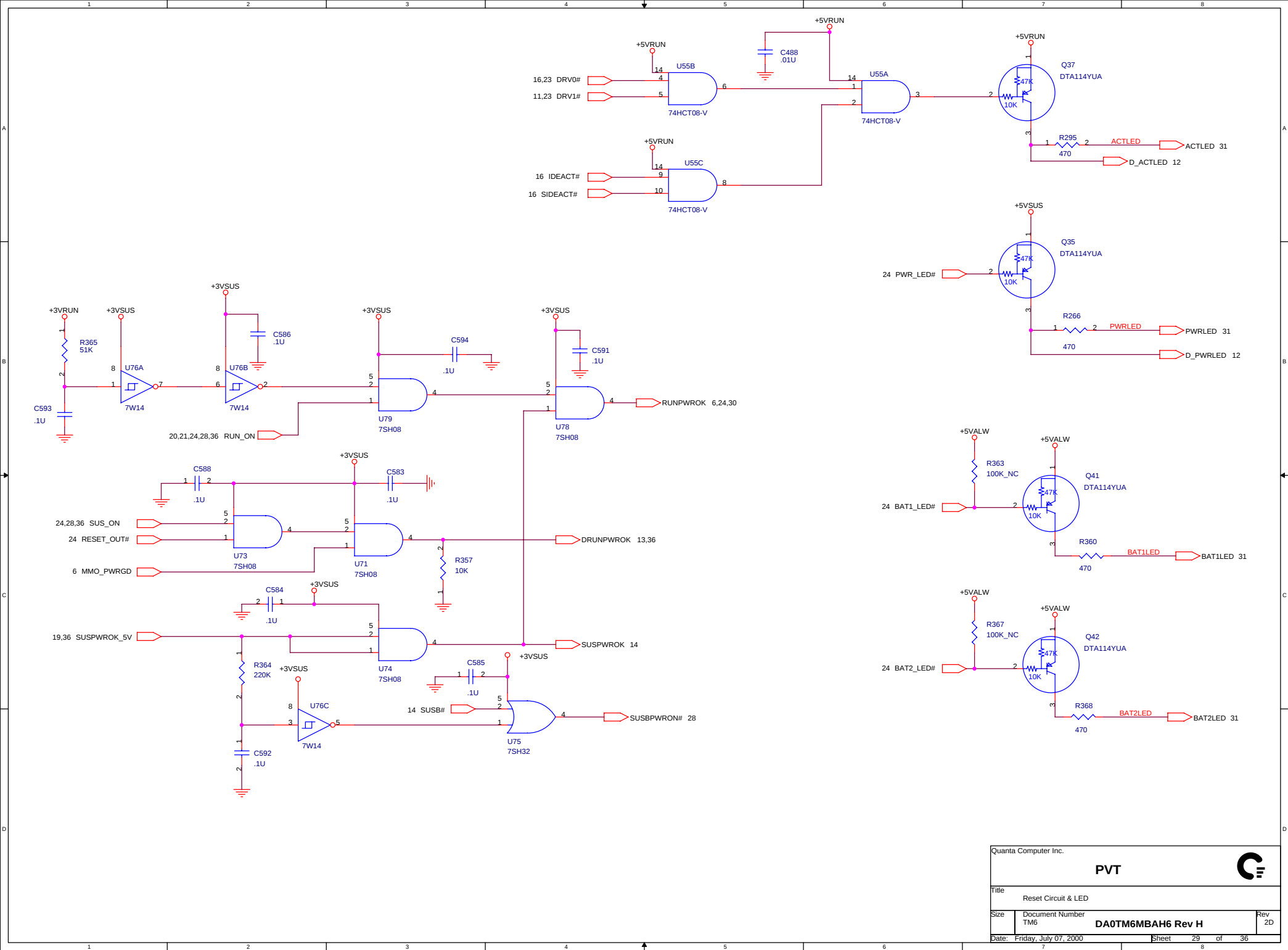


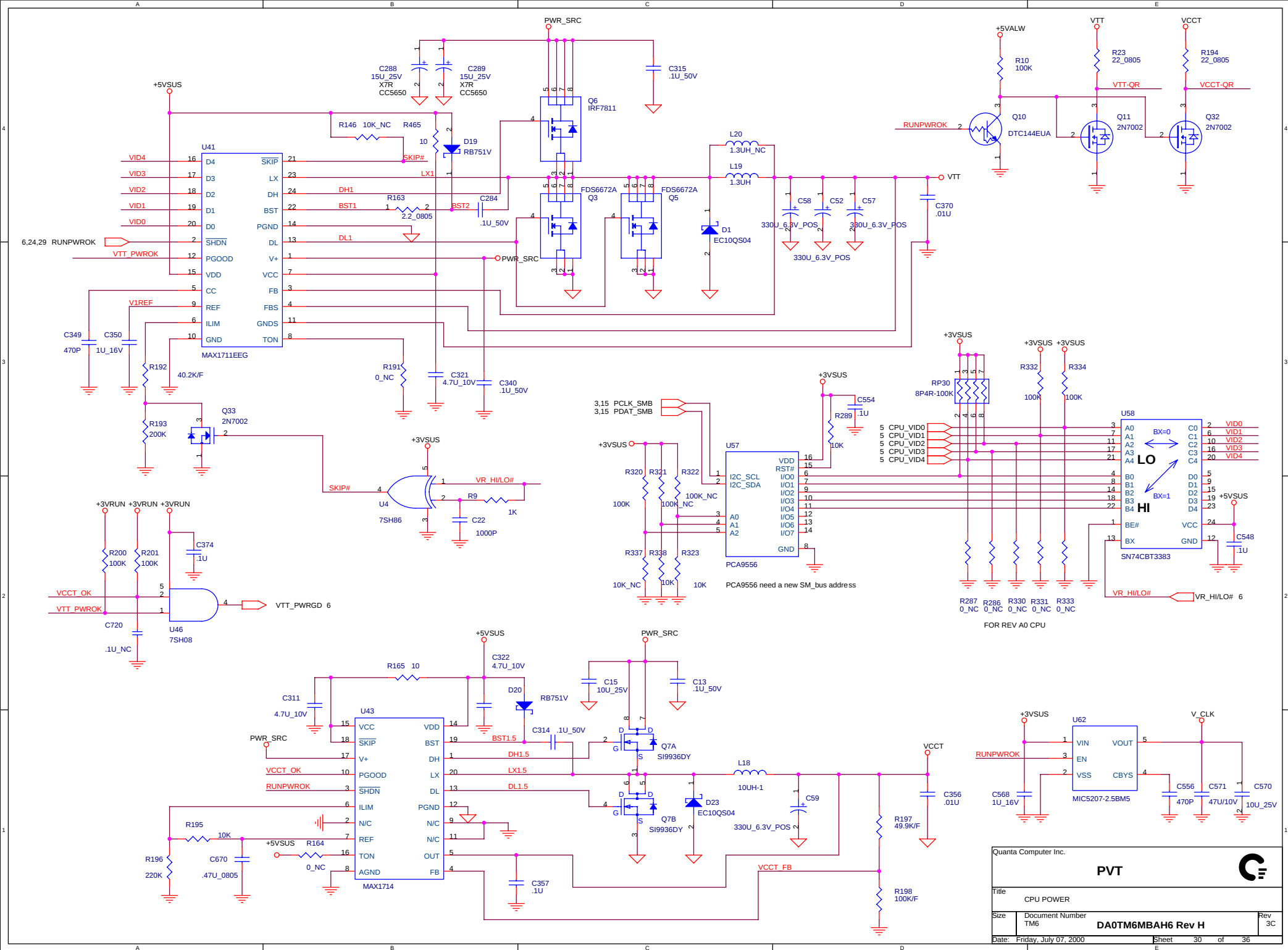
Quanta Computer Inc.		PVT	
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Stick&Touch Pad		DA0TM6MBAH6 Rev H	Rev 3C
Size	Document Number TM6		
Date: Friday, July 07, 2000	Sheet 26 of 36		

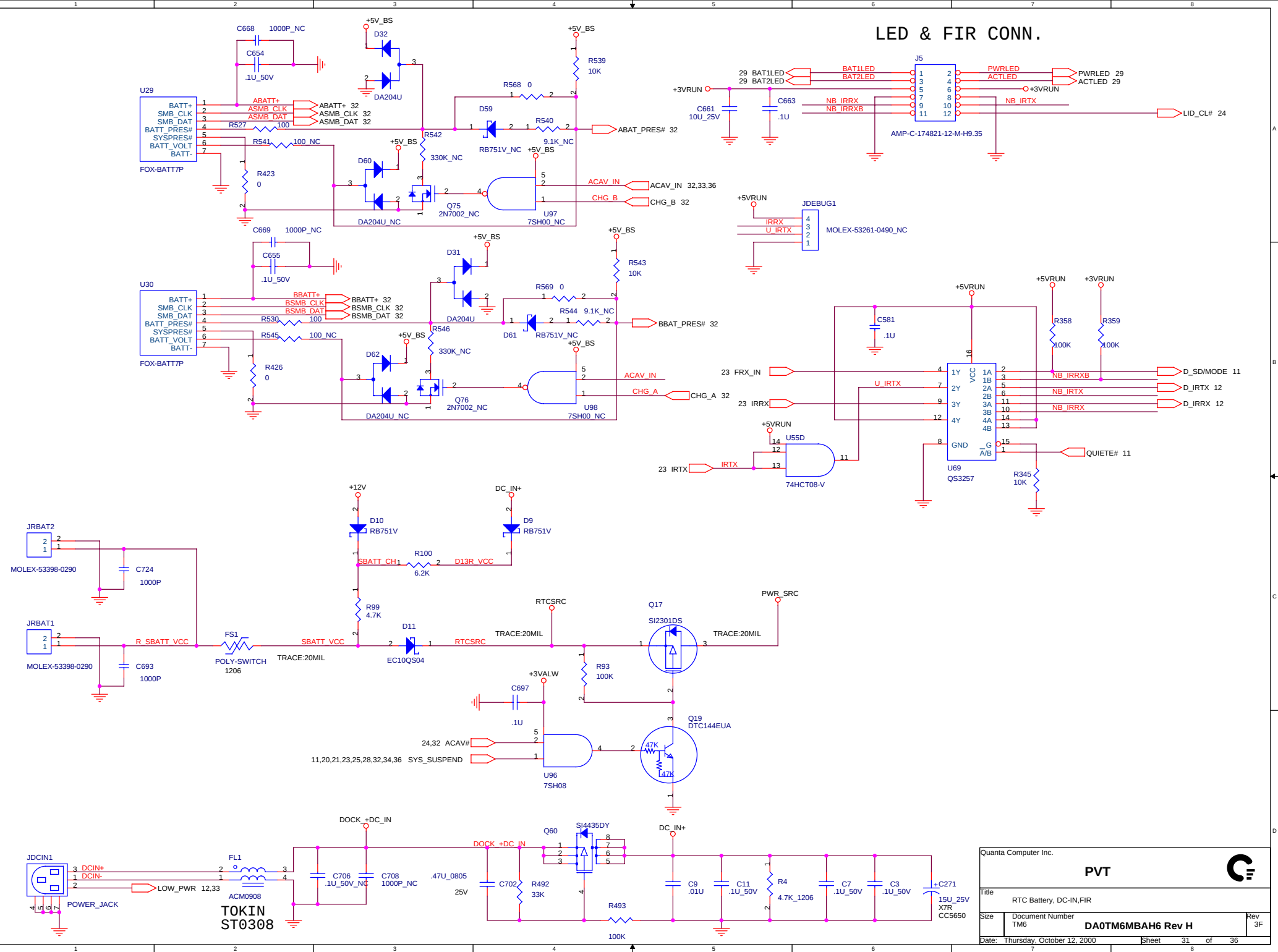


For TEAC FDD
sink current
issue



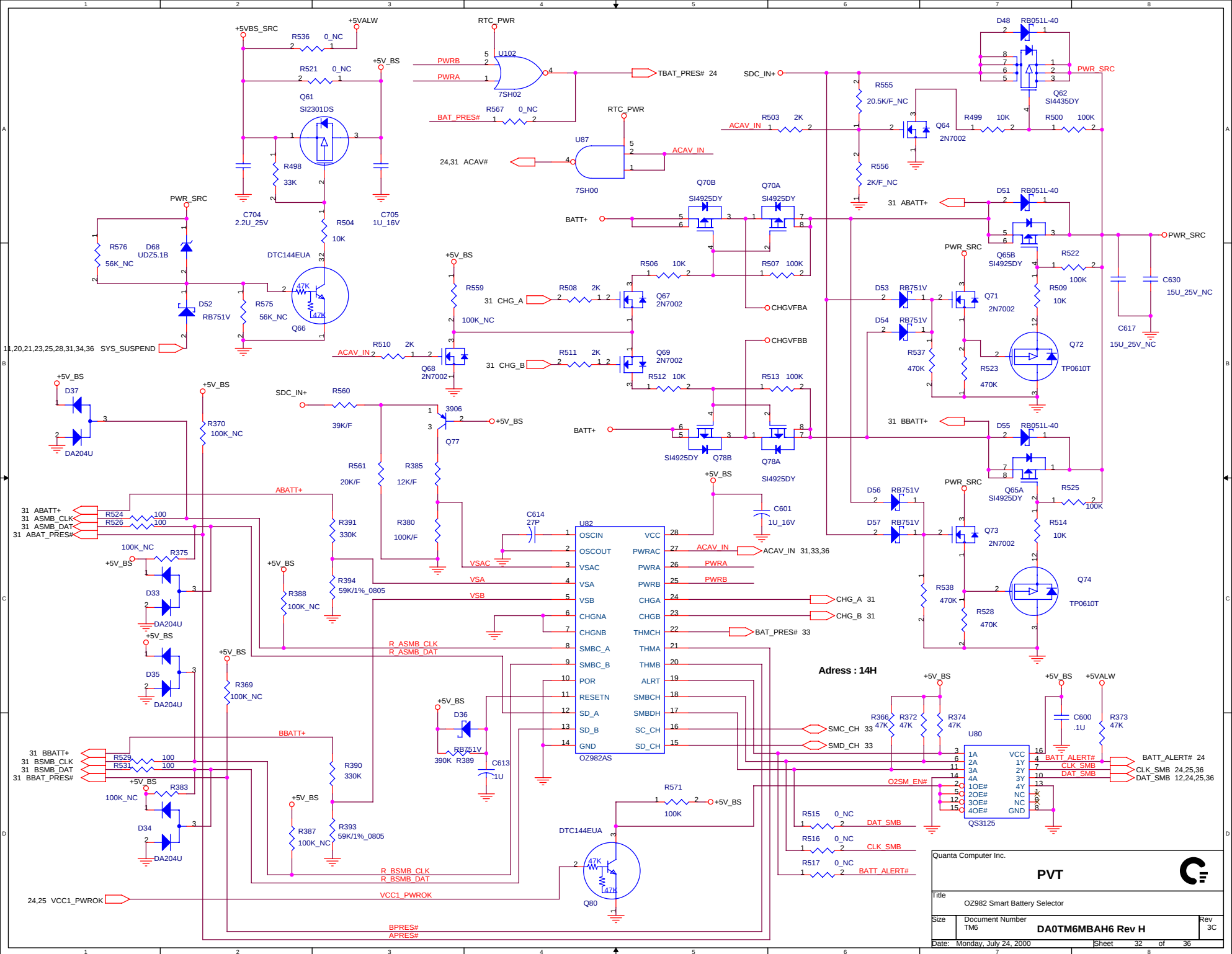






LED & FIR CONN.

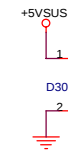
Quanta Computer Inc.			
PVT			
Title			
RTC Battery, DC-IN, FIR			
Size			
Document Number			
TM6			
Date: Thursday, October 12, 2000			
Sheet 31 of 36			
Rev 3F			



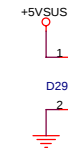


52610-0890
Molex

CLOSE TO JTV1



D30
DA204U



D28
DA204U



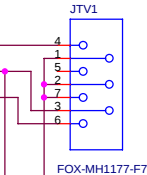
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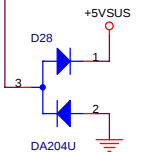
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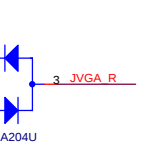
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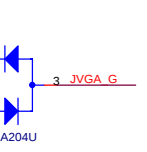
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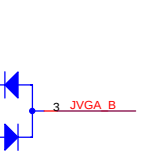
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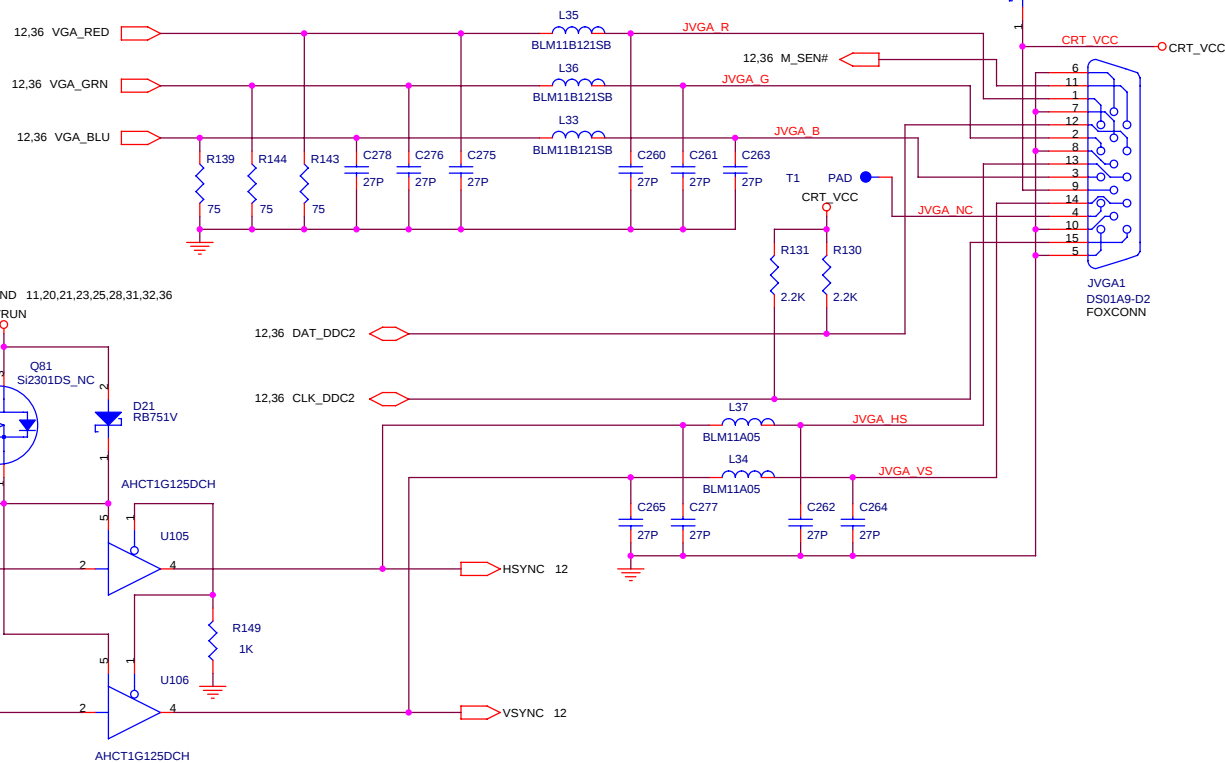
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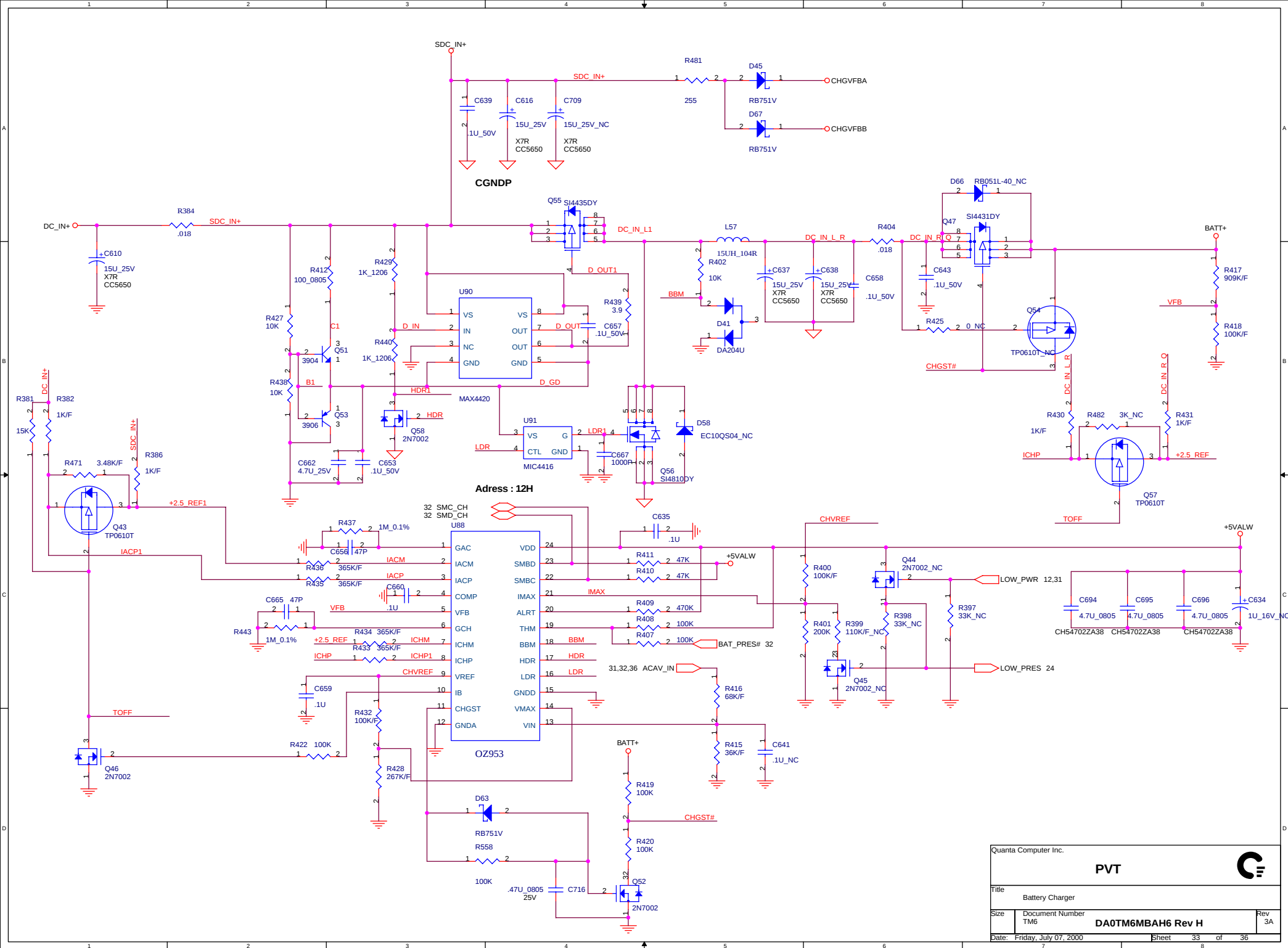
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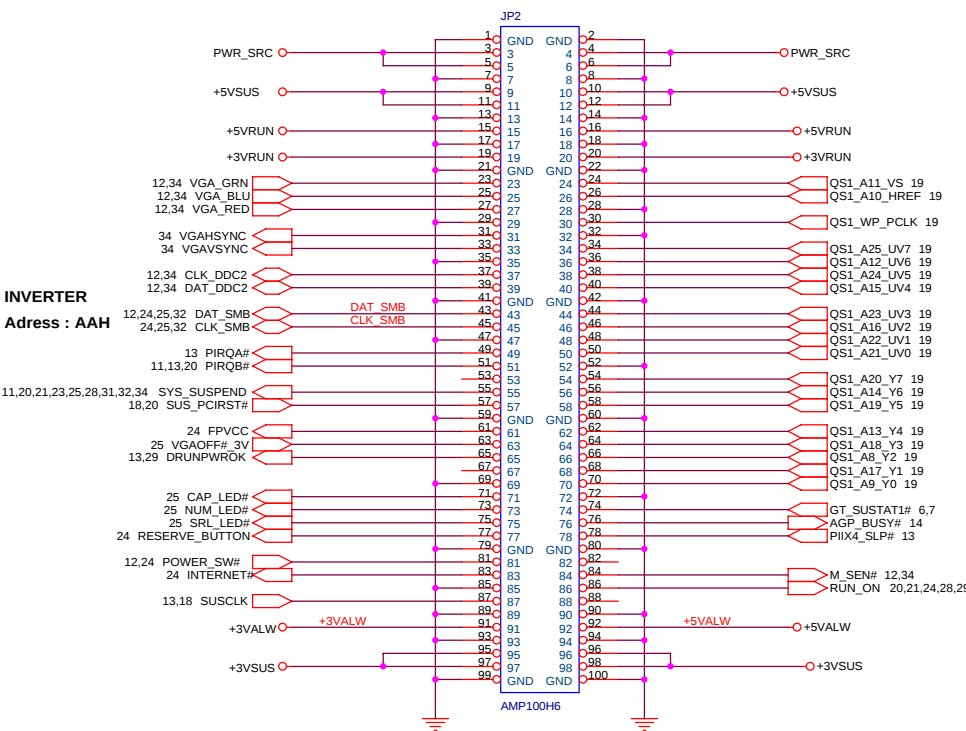


D16
DA204U



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PVT			
Title S-VIDEO & CRT			
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INVERTER

Adress : AAH

